DESCRIPTION

M57962L-01R-02 is a hybrid integrated circuit designed for driving n-channel IGBT modules in any gate-amplifier application. This device operates as an isolation amplifier for these modules and provides the required electrical isolation between the input and output with an opto-coupler.

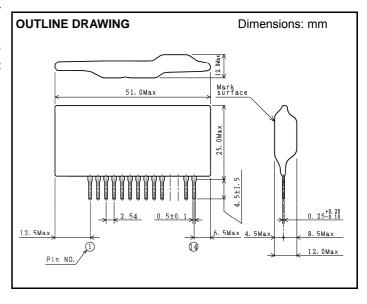
Recommended IGBT modules:

Vces = 600V series up to 600A class

VCES = 1200V series up to 400A class

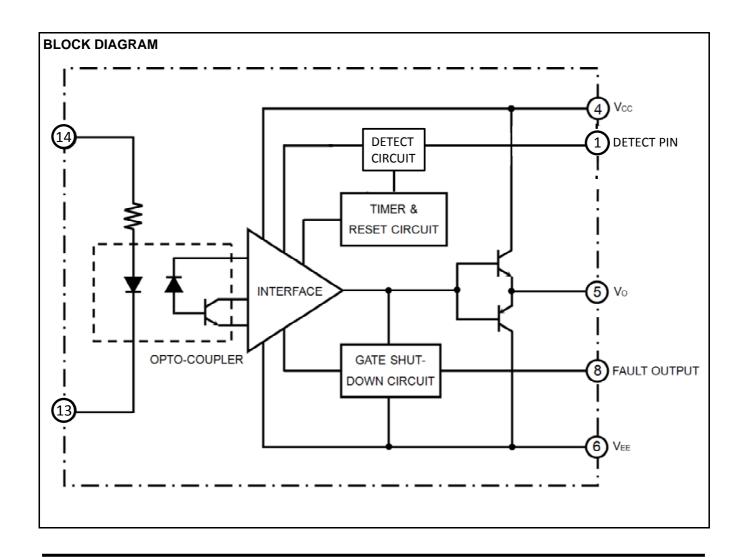
FEATURES

- •Electrical isolation between input and output with opto-coupler (Viso = 2500Vrms for 1minute)
- •Two supply driver topology
- •Built-in short circuit protection circuit (With a pin for fault out)
- •TTL compatible input interface



APPLICATIONS

To drive IGBT modules for inverter or AC servo systems application



MAXIMUM RATINGS (unless otherwise noted, Ta=25°C)

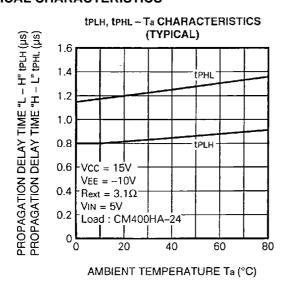
Symbol	Parameter	Conditions	Ratings	Unit
V _{CC}	Cumply voltage	DC	18	V
V _{EE}	- Supply voltage	DC	-15	V
Vı	Input voltage	Applied between: 13pin and 14pin	-1 ~ +7	V
Vo	Output voltage	When the output voltage is "H"	V _{CC}	V
I _{OHP}	Output suggest	Dulas width Over f / 2014 la	-5	Α
I _{OLP}	Output current	Pulse width $2\mu s$, $f \leq 20kHz$	5	Α
V _{iso}	Isolation voltage	Sine wave voltage 60Hz, for 1min	2500	V_{rms}
T _C	Case temperature	-	85	°C
T_{opr}	Operating temperature	No condensation allowable	-20 ~ +60	°C
T_{stg}	Storage temperature	No condensation allowable	-25 ~ +100 (*1)	°C
I _{OH}	Output current	DC	0.5	Α
I _{FO}	Fault output current	Applied 8pin	20	mA
V _{R1}	Input voltage	Applied 1pin	50	V

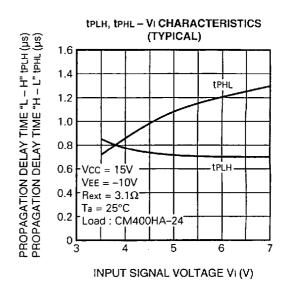
^(*1) Differs from H/C condition

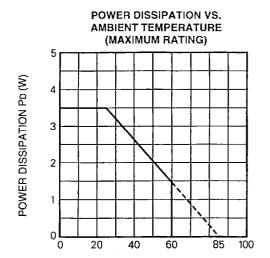
ELECTRICAL CHARACTERISTICS (unless otherwise noted, Ta=25°C, Vcc = 15V, VEE = -10V)

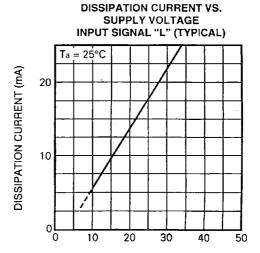
Symbol	Parameter	Conditions	Limits			Unit
			Min	Тур	Max	Uffil
V_{CC}	Supply voltage	Recommended range	14	15	-	٧
V_{EE}	Supply voltage		-7	-	-10	V
V_{IN}	Pull-up voltage on input side	Recommended range	4.75	5	5.25	V
f	Switching frequency	Recommended range	-	-	20	kHz
I _{IH}	"H" input current	V _{IN} = 5V	-	16	-	mA
R _G	Gate resistance	Recommended range	2	-	-	Ω
V_{OH}	"H" output voltage	Recommended range	13	14	-	V
V _{OL}	"L" output voltage	Recommended range	-8	-9	-	V
t _{PLH}	"L-H" propagation time	I _{IH} = 16mA	-	1	1.5	μs
t _r	"L-H" rise time	I _{IH} = 16mA	-	0.6	1	μs
t _{PHL}	"H-L" propagation time	I _{IH} = 16mA	-	1	1.5	μs
t _f	"H-L" fall time	I _{IH} = 16mA	-	0.4	1	μs
t_{timer}	Timer	Between start and cancel (under input sign "L")	1	-	2	ms
I _{FO}	Fault output current	Applied 8pin (With pull up 4.7kohm)	-	5	-	mA
V _{SC}	SC detect voltage	Collector voltage of module	15	-	-	V

TYPICAL CHARACTERISTICS

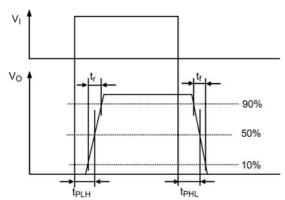








DEFINITION OF CHARACTERISTICS

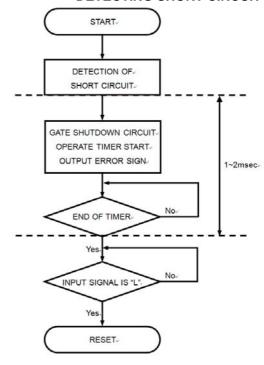


OPERATION OF PROTECTION CIRCUIT

- (1) In case the gate voltage is "H" and the collector voltage is high, this hybrid IC will recognize the circuit as short circuit and immediately reduce the gate voltage. Besides, put out an error sign ("L") which inform that protection circuit is operating at the same time from 8pin.
- (2) The protection circuit reset and resorted to ordinary condition if input sign is "L" when the premised 1~2msec passed. ("L" period needs up to 5μs.)

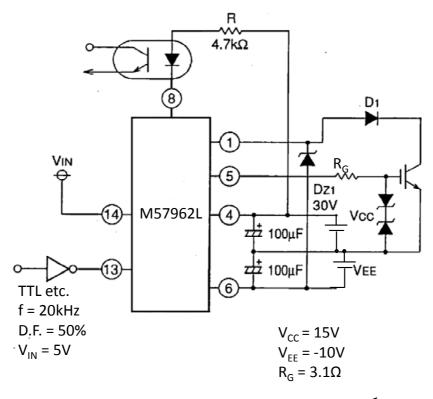
OPERATION FLOW ON

DETECTING SHORT CIRCUIT



(*) "L" output voltage with protection circuit operating is about $|V_{\text{EE}}|$ + 2V.

APPLICATION CIRCUIT EXAMPLE



D1 : FRD ($t_{rr} \leq 0.2 \mu s$)

PRECAUTION

- (1) Because the pins 2,3,7,9 and 10pin are the pins connected between surface and back, the usage with electrical connections from the outside are not permitted.
- (2) If reverse recovery time of D_1 is long, 1pin is applied high voltage. In that case, counterplan for protection which inserted a zener diode between 1 and 6pin are necessary like above diagram.
- (3) The hybrid IC will output fault sign ("L") until supply voltage become stable whether supply is ON or OFF.

FOR SAFETY USING

Great detail and careful attention are given to the production activity of Hics, such as the development, the quality of production, and in it's reliability. However the reliability of Hics depends not only on their own factors but also in their condition of usage. When handling Hics, please note the following cautions.

CAUTIONS				
Packing	The materials used in packing Hics can only withstand normal external conditions. When exposed to outside shocks, rain and certain environmental contaminators, the packing materials will deteriorates. Please take care in handling.			
Carrying	 Don't stack boxes too high. Avoid placing heavy materials on boxes. Boxes must be positioned correctly during transportation to avoid breakage. Don't throw or drop boxes. Keep boxes dry. Avoid rain or snow. Minimal vibration and shock during transportation is desirable. 			
Storage	 When storing Hics, please observe the following notices or possible deterioration of their electrical characteristics, risk of solder ability, and external damage may occur. 1) Devices must be stored where fluctuation of temperature and humidity is minimal, and must not be exposed to direct sunlight. Store at the normal temperature of 5 to 30 degrees Celsius with humidity at 40 to 60%. 2) Avoid locations where corrosive gasses are generated or where much dust accumulates. 3) Storage cases must be static proof. 4) Avoid putting weight on boxes. 			
Extended storage	When extended storage is necessary, Hics must be kept non-processed. When using Hics which have been stored for more than one year or under severe conditions, be sure to check that the exterior is free from flaw and other damages.			
Maximum ratings	To prevent any electrical damages, use Hics within the maximum ratings. The temperature, current, voltage, etc. must not exceed these conditions.			
Polarity	To protect Hics from destruction and deterioration due to wrong insertion, make sure of polarity in inserting leads into the board holes, conforming to the external view for the terminal arrangement.			

Keep safety first in your circuit designs!

• ISAHAYA Electronics Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (1)placement of substitutive, auxiliary circuits, (2)use of non-flammable material or (3)prevention against any malfunction or mishap.

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