

**Discrete IGBT** 

#### Discret IGBT (XS-series) 1200V/40A

#### Features

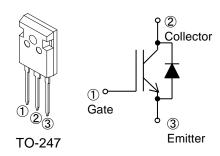
Pb-free lead terminal ;RoHS compliant Helogen-free molding compound

#### Applications

Uniterruptible Power Supply,PV Power Conditioner, Inverter welding machine



#### ■ Equivalent Circuit



#### Maximum ratings and characteristics

● Absolute maximum ratings at T<sub>vj</sub>=25°C (unless otherwise specified)

Parameter	Symbol	Value	Unit	Remarks
Collecter-Emitter voltage	V <sub>CES</sub>	1200	V	
Gate-Emitter voltage	V <sub>GES</sub>	±20	V	
Transient Gate-Emitter voltage	V GES	±30	] <b>v</b>	t <sub>p</sub> <1μs
DC collector current	I <sub>C@25</sub>	63	Α	T <sub>C</sub> =25°C
	I <sub>C@100</sub>	40	Α	T <sub>C</sub> =100°C
Pulsed collector current	I <sub>CP</sub>	160	Α	Note*1
Turn-off safe operating area	-	160	Α	$V_{CE} \le 1200 \text{ V}, T_{vi} \le 175 ^{\circ}\text{C}$
Diode forward current	I <sub>C@25</sub>	63	Α	
	I <sub>C@100</sub>	40	Α	
Diode pulsed current	I <sub>FP</sub>	160	Α	Note*1
IGBT max. power dissipation	P <sub>tot_IGBT</sub>	351	W	T <sub>C</sub> =25°C
FWD max. power dissipation	$P_{tot\_FWD}$	127	W	T <sub>C</sub> =25°C
Operating junction temperature	T <sub>vj</sub>	-40 <b>~</b> +175	°C	
Storage temperature	T <sub>stg</sub>	-55 <b>~</b> +175	°C	

Note\*1 : Pulse width limited by  $T_{\text{vimax}}$ 

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#### lacktriangle Electrical characteristics (at $T_{vj}$ = 25°C unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
Zero-gate voltage collector current	I <sub>CES</sub>	V <sub>CE</sub> =1200V	T <sub>vj</sub> =25°C	-	-	250	
		V <sub>GE</sub> =0V	T <sub>vj</sub> =175°C	-	-	2	mA
Gate-Emitter leakage current	I <sub>GES</sub>	$V_{\text{CE}}=0V$ $V_{\text{GE}}=\pm 20V$		-	-	200	nA
Gate-Emitter threshold voltage	V <sub>GE(th)</sub>	V <sub>CE</sub> =20V I <sub>C</sub> =40mA		4.9	5.5	6.1	V
Collector-Emitter saturation voltage		V <sub>GE</sub> =15V I <sub>C</sub> =40A	T <sub>vj</sub> =25°C	1.3	1.6	1.9	V
	V <sub>CE(sat)</sub>		T <sub>vj</sub> =125°C	-	2.05	-	
		76-407	T <sub>vj</sub> =175°C	-	2.15	-	
Input capacitance	C <sub>ies</sub>	$V_{\text{CE}}$ =25V $V_{\text{GE}}$ =0V		2350	4700	7050	pF
Oputput capacitance	Coes			33	66	100	
Reverse transfer capacitance	C <sub>res</sub>	f=1MHz		19	38	60	
Gate charge	Q <sub>G</sub>	V <sub>CC</sub> =600V, I <sub>C</sub> =40A, V <sub>GE</sub> =15V		125	250	380	nC
Turn-on delay time	t <sub>d(on)</sub>	T <sub>vj</sub> =25°C, V <sub>CC</sub> =600V, I <sub>C</sub> =40A		22	45	70	
Rise time	t <sub>r</sub>	$V_{\rm GE}$ =15V, $R_{\rm G}$ =10 $\Omega$ Energy loss include "tail" and FWD reverse recovery.		16	32	50	ns
Turn-off delay time	$t_{d(off)}$			125	250	380	
Fall time	t <sub>f</sub>			30	60	90	
Turn-on energy	E <sub>on</sub>			0.7	1.4	2.1	1
Turn-off energy	$E_{\text{off}}$	-		0.85	1.7	2.6	- mJ
Turn-on delay time	t <sub>d(on)</sub>	$T_{\text{vj}}$ =175°C, $V_{\text{CC}}$ =600V, $I_{\text{C}}$ =40A $V_{\text{GE}}$ =15V, $R_{\text{G}}$ =10 $\Omega$		22	44	66	
Rise time	t <sub>r</sub>			13	26	39	1
Turn-off delay time	$t_{d(off)}$	Energy loss include "tail"		140	280	420	ns
Fall time	t <sub>f</sub>	and FWD reverse recovery.		65	130	195	1
Turn-on energy	Eon			1.1	2.2	3.3	
Turn-off energy	E <sub>off</sub>	1		1.0	2.0	3.0	- mJ

 $<sup>\</sup>Re$  Recommended external  $R_{\rm G}$  value range is from 5.1Ω to 51Ω.

#### FWD characteristics

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
			T <sub>vj</sub> =25°C	2.5	2.9	3.3	
Forward voltage drop	$V_{F}$	I <sub>F</sub> =40A	T <sub>vj</sub> =125°C	-	3.2	-	V
			T <sub>vj</sub> =175°C	-	3.2	-	
Diode reverse recovery time	t <sub>rr</sub>	V <sub>CC</sub> =600V,I <sub>F</sub> =40A		115	230	345	ns
Diode reverse recovery charge	Q <sub>rr</sub>	-d <i>i<sub>F</sub></i> /d <i>t</i> =300A/μs, <i>T</i> <sub>vj</sub> =25°C		0.55	1.10	1.65	μC
Diode reverse recovery time	t <sub>rr</sub>	V <sub>CC</sub> =600V,I <sub>F</sub> =40A		250	500	750	ns
Diode reverse recovery charge	Q <sub>rr</sub>	-d <i>i</i> <sub>F</sub> /d <i>t</i> =300A/μs, <i>T</i> <sub>vj</sub> =175°C		1.15	2.30	3.45	μC

#### ■ Thermal resistance

Parameter	Symbol	Min.	Тур.	Max.	Unit
Termal resistance, junction-anbient	$R_{th(j-a)}$	-	-	50	°C/W
Termal resistance, IGBT junction to case	$R_{\text{th(j-c)\_IGBT}}$	-	-	0.427	°C/W
Termal resistance, FWD junction to case	$R_{th(j-c)\_FWD}$	-	-	1.176	°C/W

**Discrete IGBT** 

Figure 1. IGBT power dissipation vs  $T_c$  $T_{vi} \le 175 \,^{\circ}\text{C}$ 

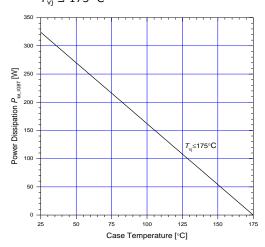


Figure 3. Typical output characteristics

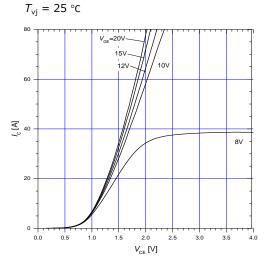


Figure 5. Typical transfer characteristics

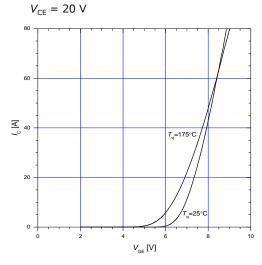


Figure 2. DC collector current vs  $T_c$ 

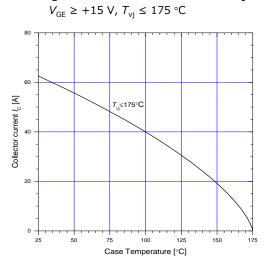


Figure 4. Typical output characteristics

$$T_{\rm vj}$$
 = 175 °C

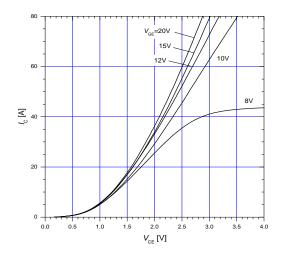
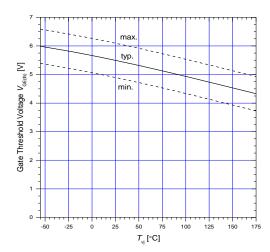


Figure 6. Gate threshold voltage

$$I_{\rm C}$$
 = 40 mA,  $V_{\rm CE}$  = 20 V



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Figure 7. Typical capacitance

$$V_{GE} = 0 \text{ V}, f = 1 \text{ MHz}$$

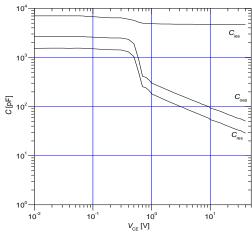


Figure 9. Typical switching times vs.  $I_{\rm C}$   $V_{\rm CC}$  = 600 V,  $V_{\rm GE}$  = 15 V,  $R_{\rm G}$  = 10  $\Omega$ ,  $T_{\rm vj}$  = 175 °C

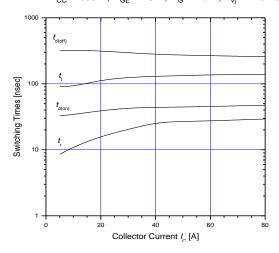


Figure 11. Typical switching losses vs.  $I_C$   $V_{CC}$  = 600 V,  $V_{GE}$  = 15 V,  $R_G$  = 10  $\Omega$ ,  $T_{vj}$  = 175 °C

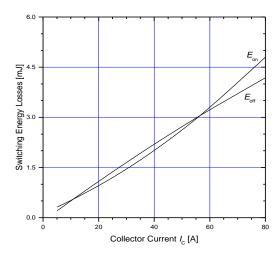


Figure 8. Typical gate charge  $I_{\rm C}$  = 40 A,  $V_{\rm CC}$  = 600 V,  $T_{\rm vi}$  = 25 °C

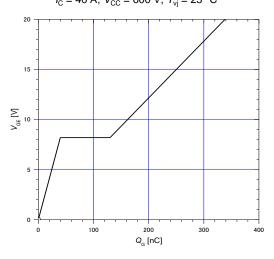


Figure 10. Typical switching times vs.  $R_G$  $V_{CC}$  = 600 V,  $V_{GE}$  = 15 V,  $I_C$  = 40 A,  $T_{vj}$  = 175 °C

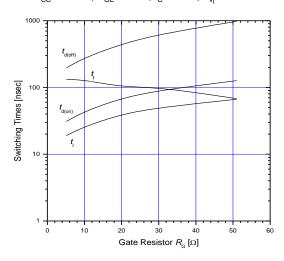
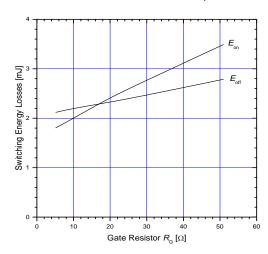


Figure 12. Typical switching losses vs.  $R_G$  $V_{CC} = 600 \text{ V}$ ,  $V_{GE} = 15 \text{ V}$ ,  $I_C = 40 \text{ A}$ ,  $T_{vj} = 175 ^{\circ}\text{C}$ 





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Figure 13. Typical forward characteristics of FWD

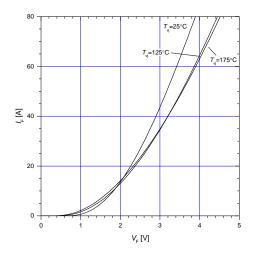


Figure 15. Typical reverse recovery loss vs.  $I_F$   $V_{CC}$  = 600 V,  $V_{GE}$  = 15 V,  $R_G$  = 10  $\Omega$ ,  $T_{v_i}$  = 175 °C

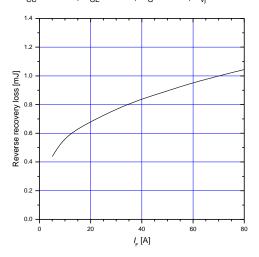


Figure 17. Transient Thermal Impedance of IGBT

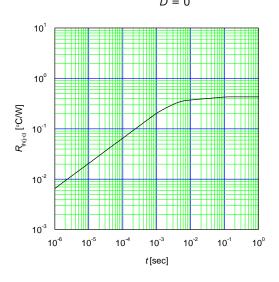


Figure 14. Typical reverse recovery characteristics vs.  $I_F$   $V_{\rm CC}$  = 600 V,  $V_{\rm GE}$  = 15 V,  $R_{\rm G}$  = 10  $\Omega$ ,  $T_{\rm vj}$  = 175 °C

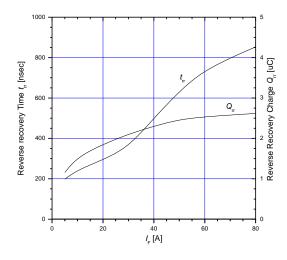


Figure 16. Reverse biased safe operating area  $V_{\rm GE}$  = 15 V / 0 V,  $R_{\rm G}$  = 10  $\Omega$ ,  $T_{\rm vi}$  ≤ 175 °C

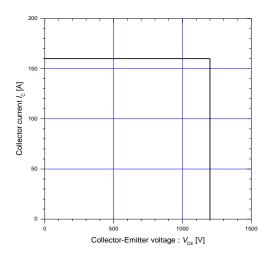
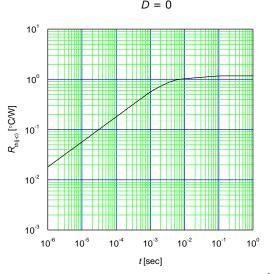


Figure 18. Transient Thermal Impedance of FWD



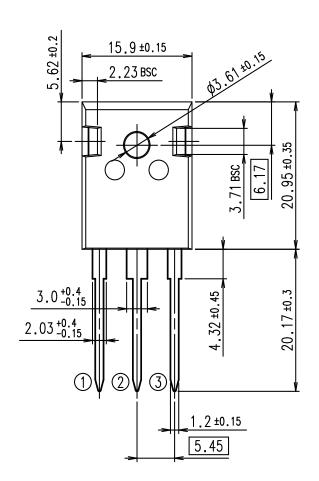
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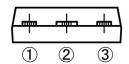


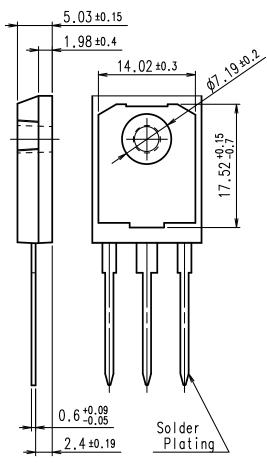
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#### Outline drawings,mm

Outview: TO-247 package







Connection

- 1) Gate
- 2 Collector
- 3 Emitter

Dimensions are in millimeters.

**IGBT Modules** 

### **Warnings**

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